

Automata over infinite alphabets: Investigations in Fresh-Register Automata

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Logical Foundations of Data Science, UCL, Nov 2015

Supported by a Royal Academy of Engineering Research Fellowship

infinite alphabets & program behaviour

```
public void foo() {  
    // Create new list  
    List x = new ArrayList();  
  
    x.add(1); x.add(2);  
    Iterator i = x.iterator();  
    Iterator j = x.iterator();  
    i.next(); i.remove(); j.next();  
}
```

infinite alphabets & program behaviour

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```

Programs with usage of resources/names can go beyond finite alphabets (cf. modelling/analysis of programs)
– but in a *parametric way*

What this talk is about

This talk is about an automata model over infinite alphabets akin to finite-state automata:

finite-state + registers + freshness oracles

We give an overview of their expressiveness & talk about

- emptiness, closures
- bisimilarity
- extensions (pushdown, classes/histories)

Automata for infinite alphabets

Let $\Sigma = \{a_1, a_2, \dots, a_n, \dots\}$ be an infinite alphabet of names

*can only be
compared
for equality*

Automata for infinite alphabets

Let $\Sigma = \{a_1, a_2, \dots, a_n, \dots\}$ be an **infinite** alphabet of **names**

- examine languages over Σ^*
 - or, languages over $(F \cup \Sigma)^*$
 - or, languages over $(F \times \Sigma)^*$
 - usually called *data words* (XML)
- look for notions of regularity, CFGs, etc.
- devise effective algorithms for reachability, membership, etc.

can only be compared for equality

a finite set of **constants**

many (finitely many) automata models

History-Dependent Automata

- π -calculus models, “named sets”, symmetries, bisimulation

[Montanari & Pistore '98, Pistore '99; Montanari & Pistore '00, Ferrari, Montanari & Pistore '02]

Register Automata (aka FMA)

- FSAs with registers, regularity, data words & XML, extensions

[Kaminski & Francez '94, Neven, Schwentick & Vianu '04]

[Sakamoto & Ikeda '00, Demri & Lazić '09; Libkin, Tan & Vrgoc '15; Jurdzinski & Lazić '11, Figueira '12]

[Cheng & Kaminski '98, Segoufin '06]

[Bojańczyk, Muscholl, Schwentick, Segoufin & David '06, Bjorklund & Schwentick '10]

Nominal Automata

- Finite \rightarrow finite orbit, used on nominal sets & other group actions

[Bojańczyk, Klin & Lasota '11, '14]

Register Automata (RA)

Let $\Sigma = \{a_1, a_2, \dots, a_n, \dots\}$ be an **infinite** alphabet of **names**



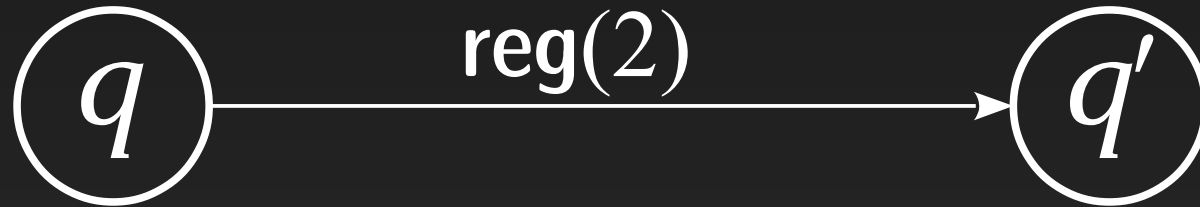
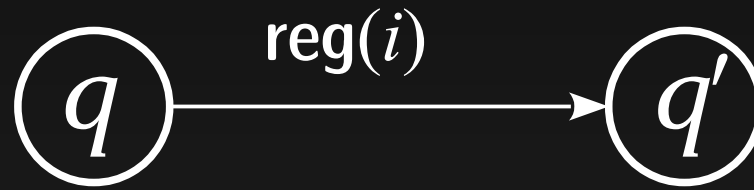
finitely many
(say R) **registers**

registers store names

Label λ of the form:

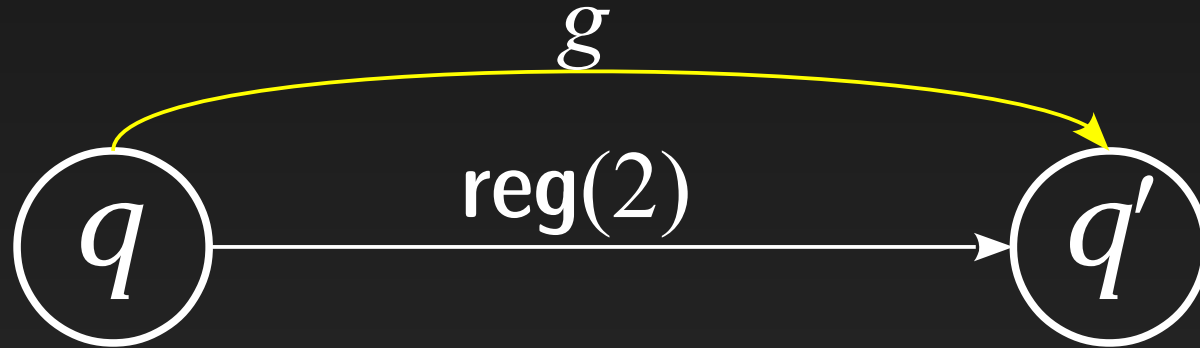
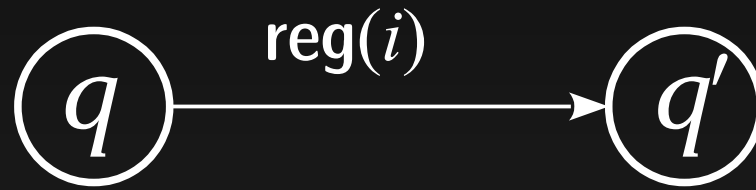
- **reg**(i), $i \in \{1, \dots, R\}$
- **diff**(i), $i \in \{1, \dots, R\}$

Transitions:



a	g	b
-----	-----	-----

Transitions:



a g b

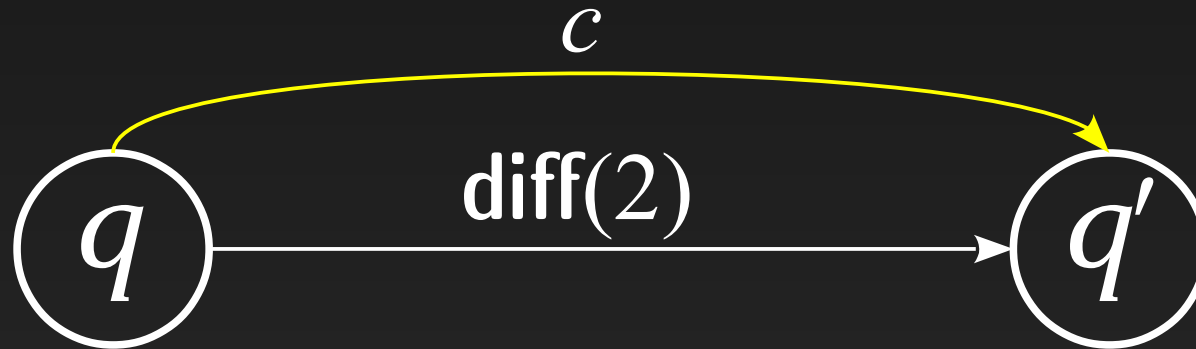
a g b

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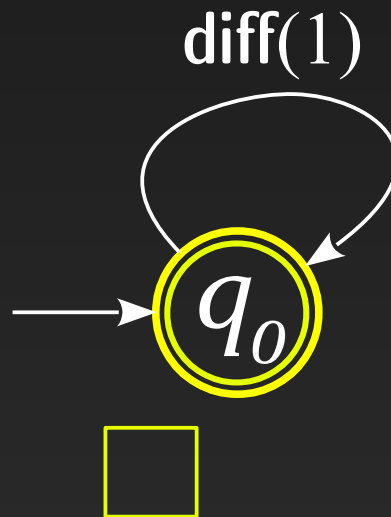


*different from
current registers*

Example

$$L_1 = \{ a_1 a_2 \dots a_n \in \Sigma^* \mid n \geq 0, \forall i < n. a_i \neq a_{i+1} \}$$

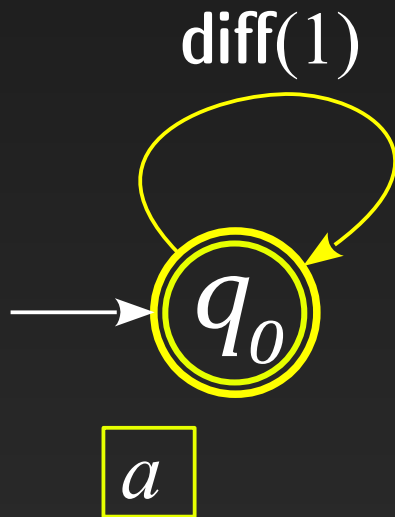
(all strings where each name is distinct from its predecessor)



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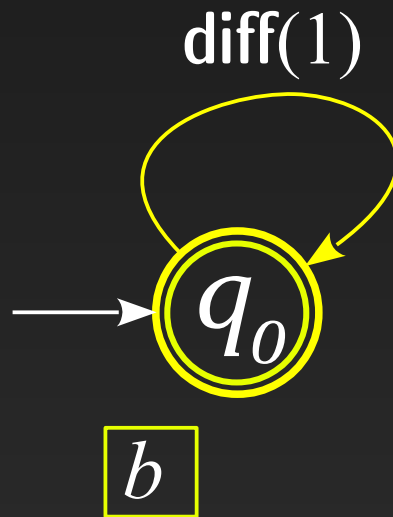


a

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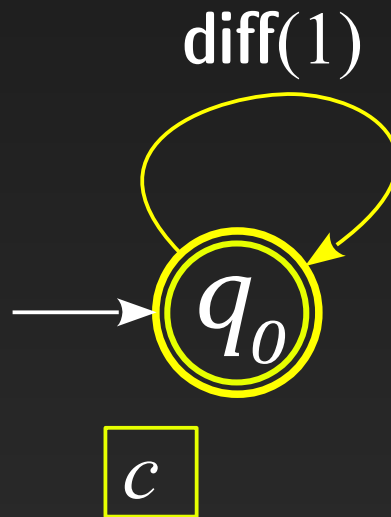


ab

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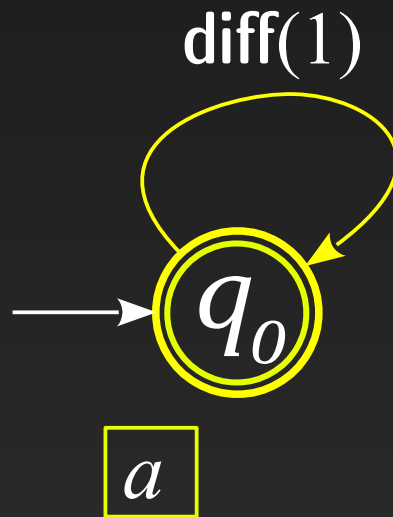


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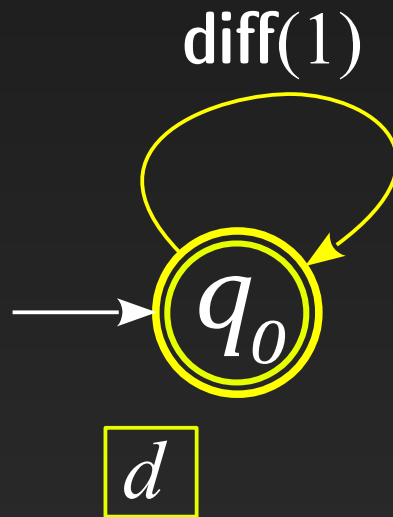


abca

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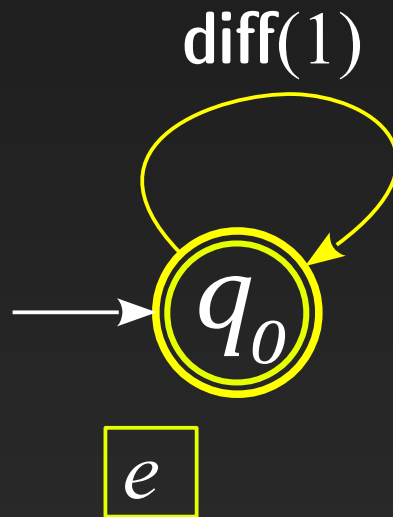


abcd

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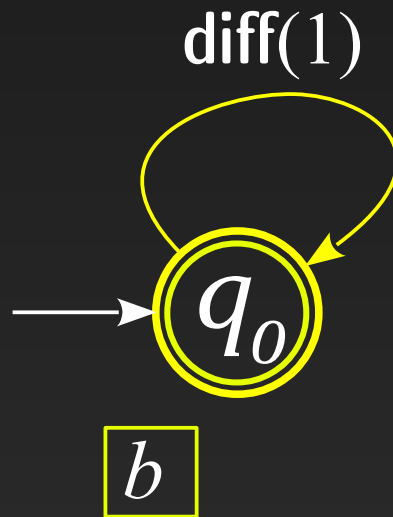


abcade

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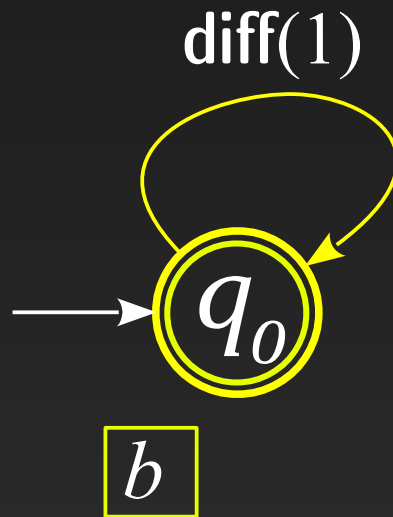


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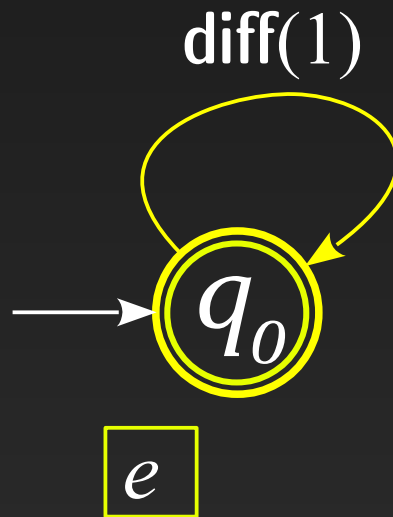


abcadebagcab

Example

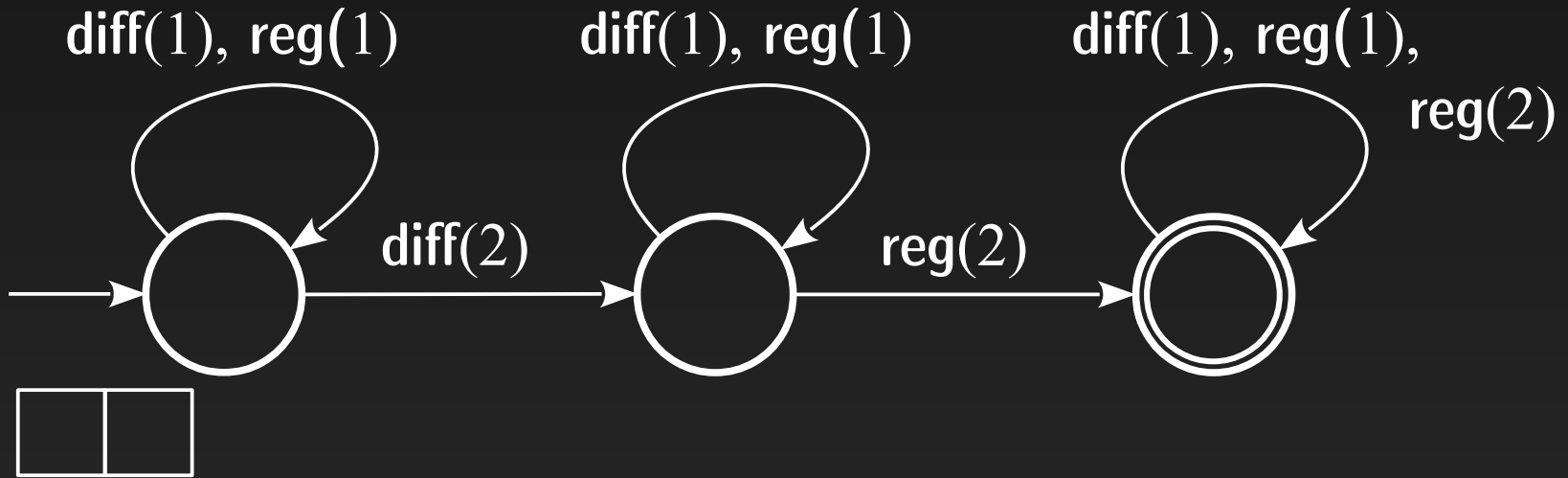
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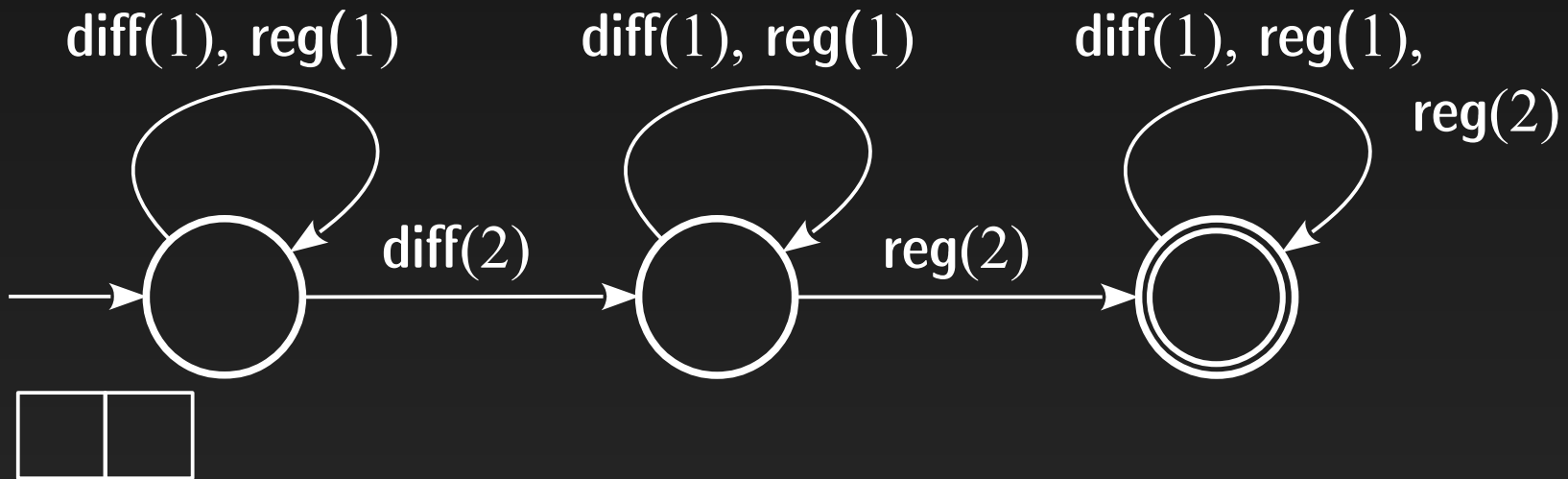


abcadebagcab and we love cake

Quiz



Quiz



$$L_2 = \{ a_1 a_2 \dots a_n \in \Sigma^* \mid n \geq 0, \exists i \neq j. a_i = a_j \}$$

(all strings where some name appears twice)

$$L_{\text{fr}} = \{ a_1 a_2 \dots a_n \in \Sigma^* \mid n \geq 0, \forall i \neq j. a_i \neq a_j \}$$

(all strings of pairwise distinct names)

– what about the complement of L_{fr} ? And that of $L_{\text{fr}} \cdot L_{\text{fr}}$?

RA properties

- Capture regularity when Σ restricted to finite
 - Closed under $\cup, \cap, \cdot, *$.
 - not closed under complement & not determinisable

[Kaminski & Francez '94]

- Universality / equivalence undecidable

[Neven, Schwentick & Vianu '04]

- Decidable emptiness:

- complexity depends on **register “mode”** (NL \rightarrow NP \rightarrow PSPACE)

[Sakamoto & Ikeda '00; Demri & Lazić '09]

- Can only truly distinguish between $R+1$ names

Example revisited

```
public void foo() {  
    // Create new list  
    List x = new ArrayList();  
  
    x.add(1); x.add(2);  
    Iterator i = x.iterator();  
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}
```

here is a safety property φ :

*if an iterator modifies its collection x
then other iterators of x become invalid*

e.g. the code on the left is bad.

We can express such “chaining”
properties using RAs

- and dynamically verify them

[Grigore, Distefano, Petersen & T. '13]

Example revisited

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public void foo() {  
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but we cannot capture **new**!

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Fresh-Register Automata (FRA)

Let $\Sigma = \{a_1, a_2, \dots, a_n, \dots\}$ be an **infinite** alphabet of **names**



*finitely many
(say R) registers*

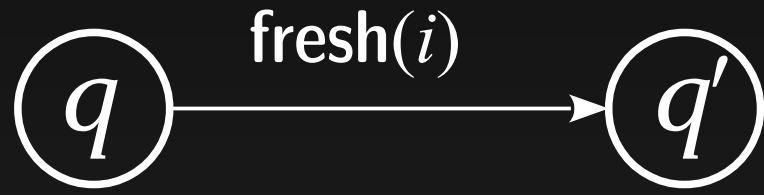
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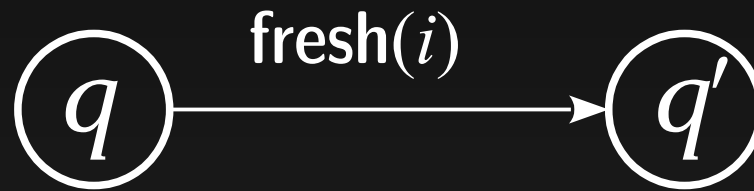
- **reg**(i), $i \in \{1, \dots, R\}$
- **diff**(i), $i \in \{1, \dots, R\}$
- **fresh**(i), $i \in \{1, \dots, R\}$

global freshness oracle

Transitions:



Transitions:

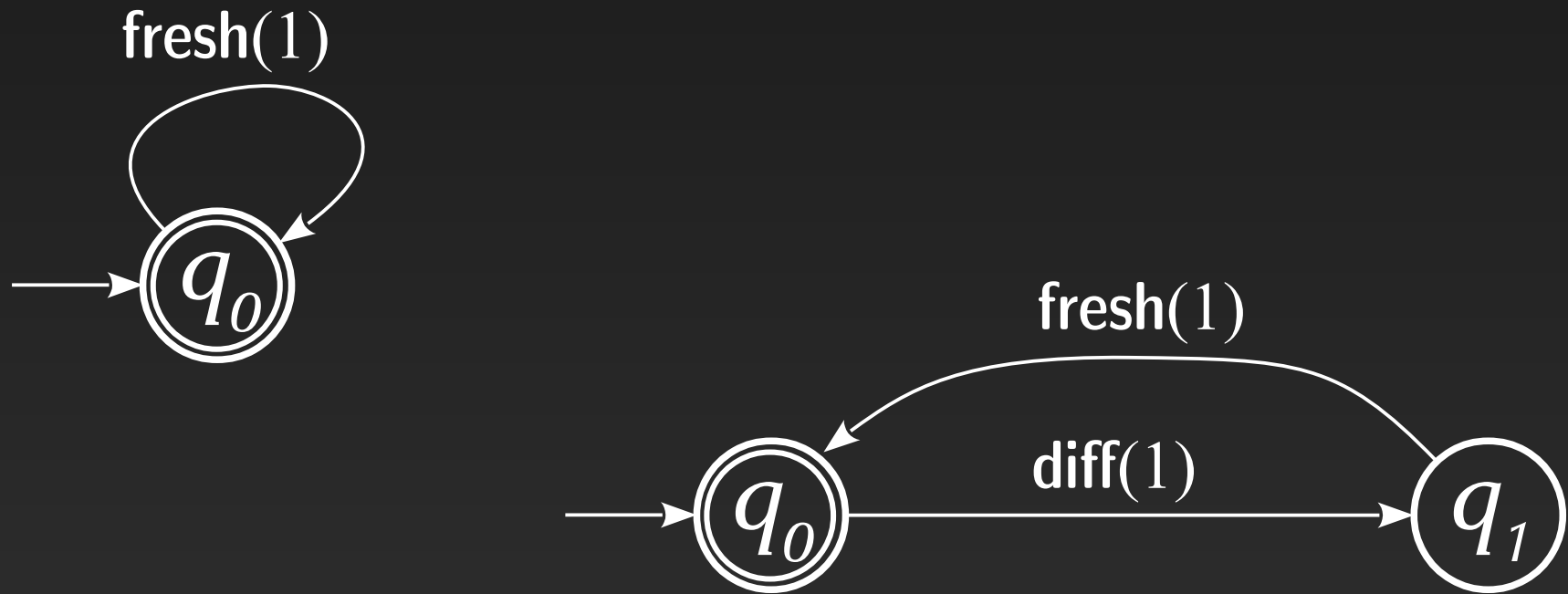


globally fresh

Examples

$$L_{\text{fr}} = \{ a_1 a_2 \dots a_n \in \Sigma^* \mid n \geq 0, \forall i \neq j. a_i \neq a_j \}$$

(all strings of pairwise distinct names)



$$L_3 = \{ a_1 a_2 \dots a_{2n} \in \Sigma^* \mid n \geq 0, \forall i < 2n. a_i \neq a_{i+1} \\ \forall i \leq n, j < 2i. a_j \neq a_{2i} \}$$

FRA properties

- Not closed under complement & not determinisable
 - Closed under \cup, \cap , but not under $\cdot, *$
- Universality / equivalence undecidable (from RAs)
- Decidable emptiness (same as RAs):
 - complexity depends on **register "mode"** (NL \rightarrow NP \rightarrow PSPACE)
- **Bisimilarity**: decidable [T.11], complexity open

FRA for program equivalence

The modelling power of FRAs can be used to model resourceful programs via **game semantics**

Program \rightarrow game model \rightarrow FRA

effectively:

two programs
are equivalent



their FRAs are language
equivalent / bisimilar

what we get:

- decision procedures for ML fragments
- same for Interface Middleweight Java

[Murawski & T. '11, '12]

[Murawski, Ramsay & T. '15]

<http://bitbucket.org/sjr/coneqct/wiki/Home>

More applications and variants

History-Dependent Automata

- freshness via “black holes” (histories)
- verification of LTL + allocation

[Pistore '99, Distefano, Rensink & Katoen '02, '04]

Session automata and learning

- freshness, but no **diff**
- canonical forms, decide equivalence

[Bollig, Habermehl, Leucker & Monmege '14]

Kleene algebras for languages with binders

- NKA: KA with ν -binder \rightarrow match with automata

[Gabbay & Ciancia '11; Kozen, Mamouras, Petrisan & Silva '15]

Investigations in FRAs

Bisimilarity for FRAs (complexity)

- Depends on register mode ($NP \rightarrow PSPACE \rightarrow EXPTIME$)
 - approach uses permutation group theory [Murawski, Ramsay & T. '15]

Context-freeness: Pushdown FRA

[Cheng & Kaminski '98; Segoufin '06]

[Murawski & T. '12]

- Reachability EXPTIME-complete
- Global reachability via “saturation”

[Murawski, Ramsay & T. '14]

Freshness oracle: from one to many histories

- History Register Automata (cf. DA/CMA)

[Grigore & T. '13]

Semantics formally: configurations

Semantics of FRAs given by **configuration graphs**:

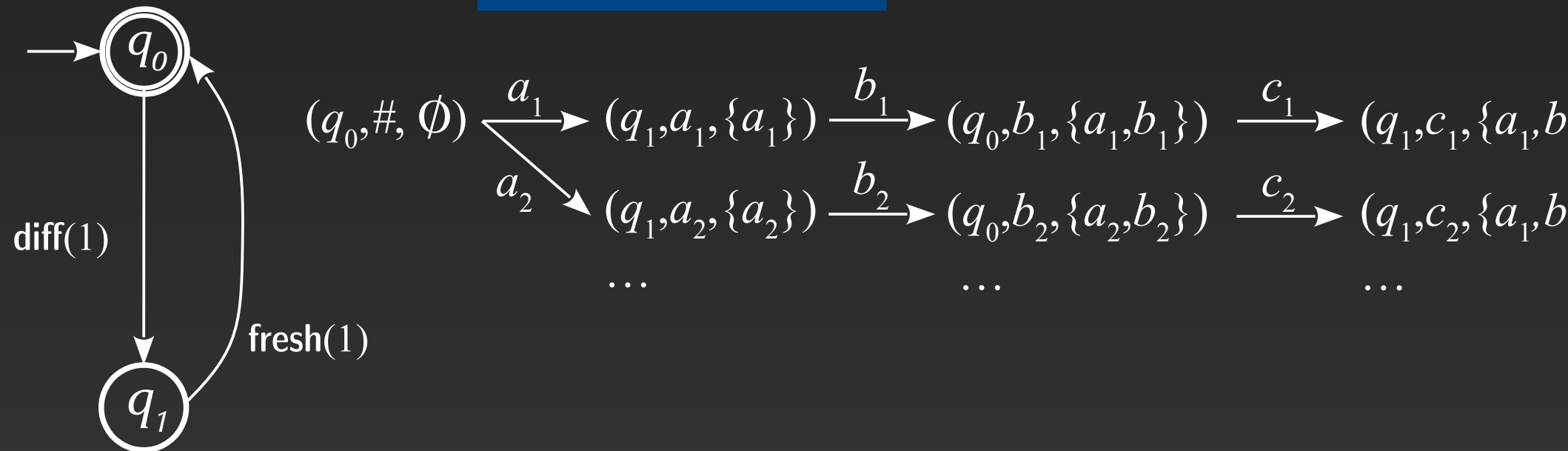
configuration

$$(q, \rho, H) \xrightarrow{a} (q', \rho', H')$$

state

register assignment:
 $\rho : \{1, \dots, R\} \rightarrow \Sigma \cup \{\#\}$

history: $H \subseteq_{\text{fin}} \Sigma$



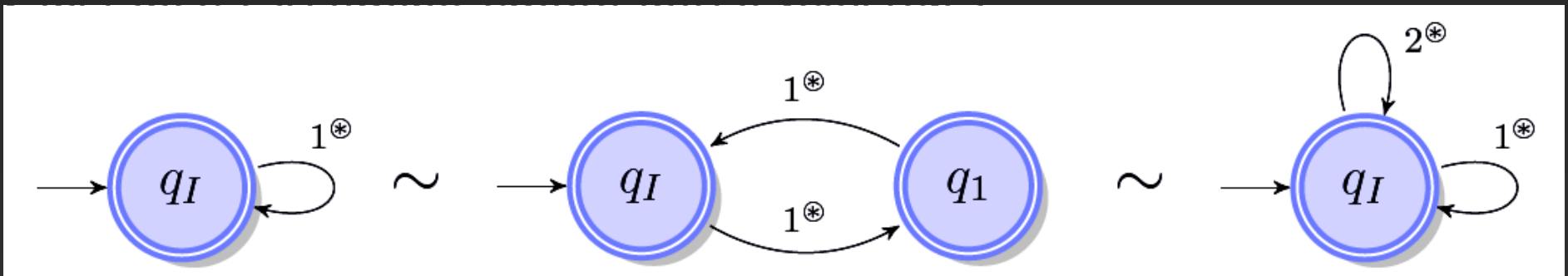
Bisimilarity

A **behavioural** notion of equivalence:

two configurations κ_1, κ_2 are bisimilar ($\kappa_1 \sim \kappa_2$)
if they can simulate one another name-by-name

We say that two FRAs are bisimilar if their initial configurations are (in the combined conf. graph).

e.g. (writing 1^* for **fresh**(1)):



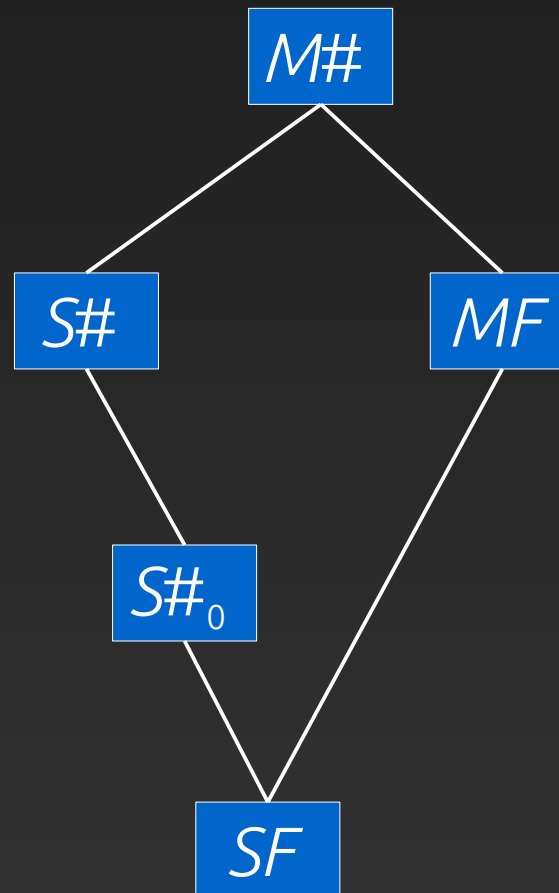
A small detail: register modes

So far we assumed: registers **initially empty**, not possible to **erase** them or have name **duplicates**.

We can generalise:

Name multiplicity

- (S) single
- (M) multiple



Register fullness

- (F) full
- (#₀) initially empty
- (#) eraseable

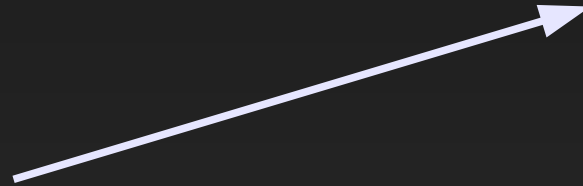
is for empty register content

$(S\#)$ \rightarrow (MF)

no multiplicities,
but erasing allowed

multiplicities, but
no empty registers

<i>a</i>	<i>g</i>	<i>#</i>	<i>b</i>	<i>#</i>
----------	----------	----------	----------	----------



<i>z</i>	<i>a</i>	<i>g</i>	<i>z</i>	<i>b</i>	<i>z</i>
----------	----------	----------	----------	----------	----------

neat, but erasing
gives exponentially
large labels

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<i>a</i>	<i>g</i>	<i>#</i>	<i>b</i>	<i>#</i>
----------	----------	----------	----------	----------

<i>z</i>	<i>a</i>	<i>g</i>	<i>z</i>	<i>b</i>	<i>z</i>
----------	----------	----------	----------	----------	----------

neat, but erasing
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<i>a'</i>	<i>g'</i>	<i>c</i>	<i>b'</i>	<i>d</i>	<i>a</i>	<i>g</i>	<i>c</i>	<i>b</i>	<i>d</i>
-----------	-----------	----------	-----------	----------	----------	----------	----------	----------	----------

concise, as each
name appears
at most twice

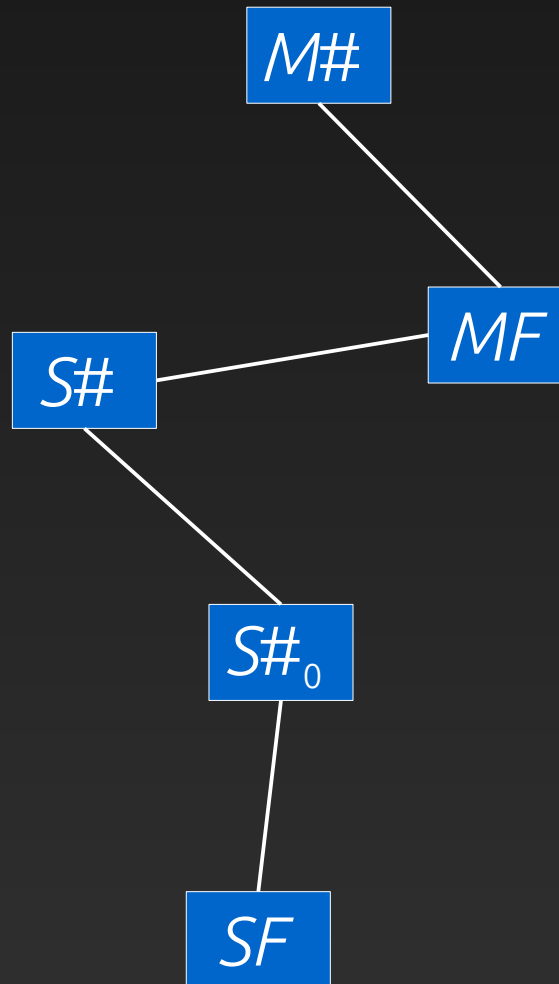
Complexity Picture

Multiplicity:

- (*S*) single
- (*M*) multiple

Fullness:

- (*F*) full
- ($\#_0$) initially empty
- ($\#$) eraseable



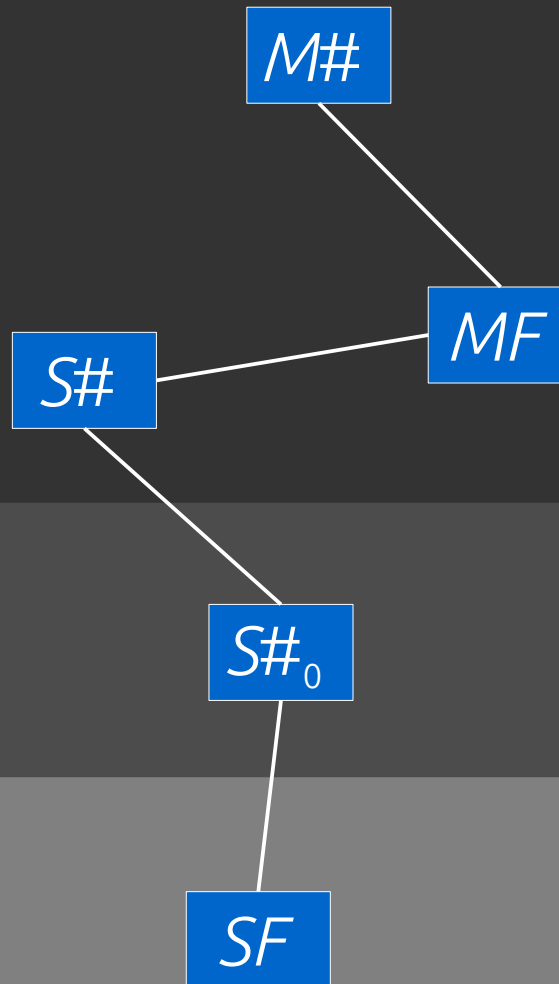
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EXPTIME-c

PSPACE-c

NP

EXPTIME solvability

To decide bisimilarity of two configurations of size R :

- we need $2R$ names to represent all possible name matchings between them
- plus one name that stands for “different”
- and another one for “fresh”

→ $2R+2$ names, that we can encode inside states:

$$Q \longrightarrow Q \times (2R+2)^R$$

(bisimilarity for finite-state automata is in PTIME)

Complexity Picture

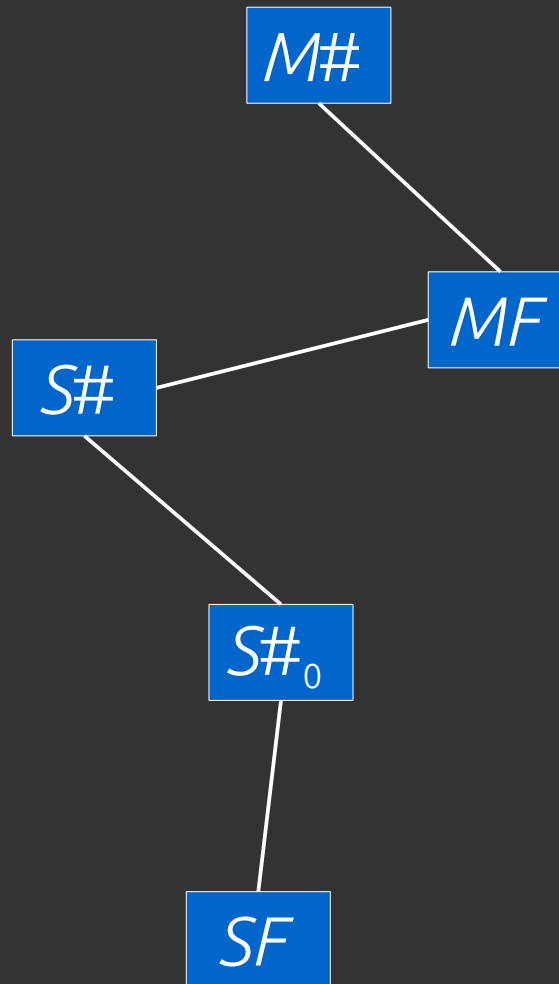
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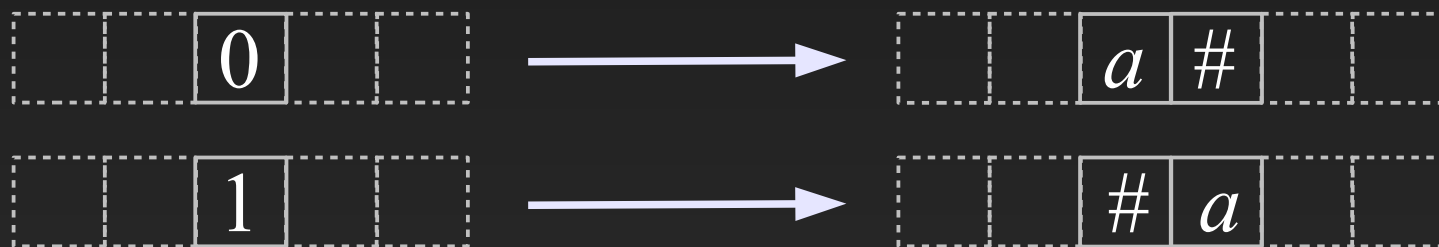
EXPTIME



EXPTIME hardness

The ($S\#$) case is EXPTIME-hard:

- reduce from alternating TMs with linear-size tape (ALBA)
- model each cell by two registers:



- arrange for non-bisimilarity at rejecting final states
- Bisimulation game (**Attacker (A)** vs **Defender (D)**):
 - **A** controls universal states, **D** controls existential ones
 - use *Defender forcing* [Jancar & Srba '08]

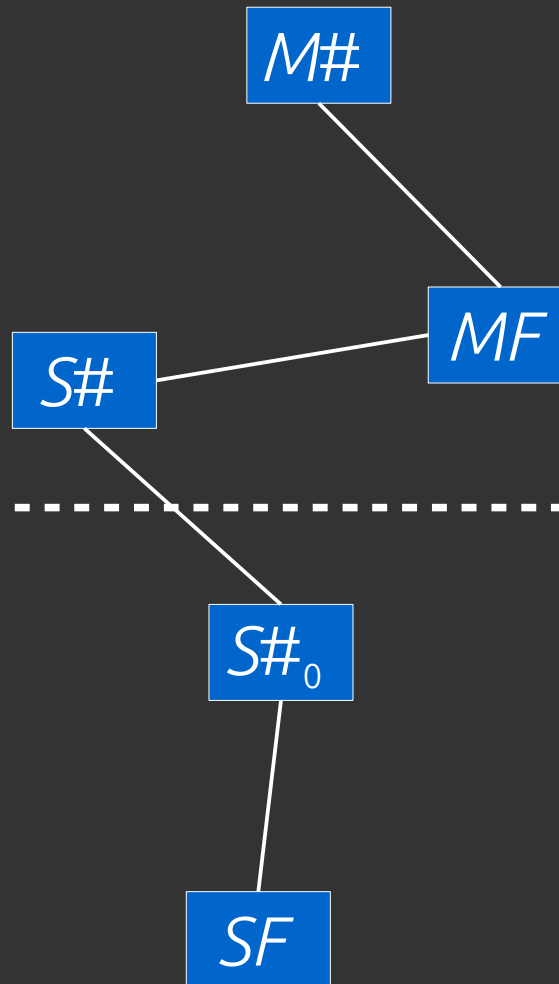
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EXPTIME

EXPTIME

The original case ($S\#_0$)

Disallowing erasures makes impossible our modelling of a linear-size tape...

In fact, the problem is PSPACE complete

First, we can model boolean assignments (cf. write-once tape), which are enough for PSPACE-hardness:

- we reduce from QBF
- Attacker chooses universal variables
- Defender chooses existential ones (via forcing)

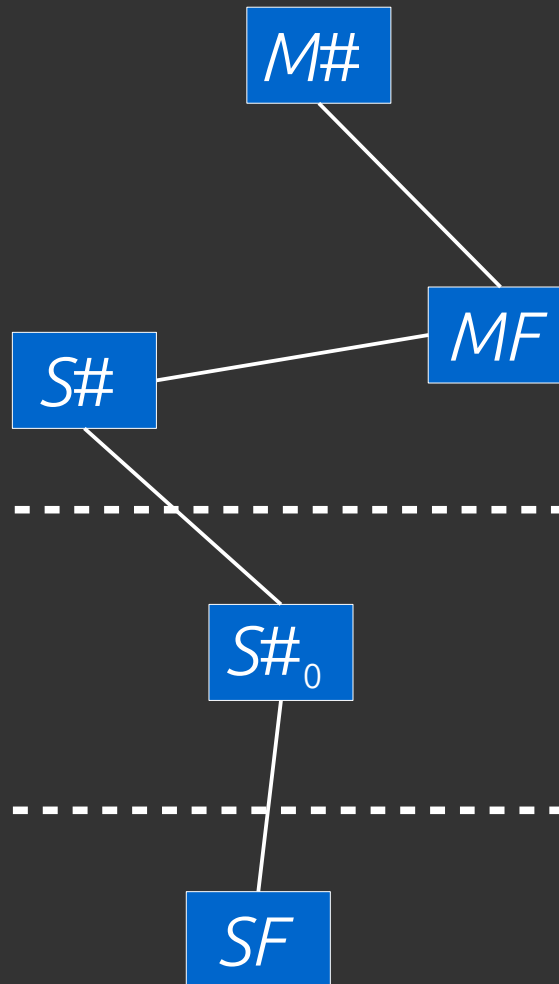
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EXPTIME

EXPTIME

PSPACE

PSPACE solvability: difficult

Our best bet is $\text{APTIME} = \text{PSPACE}$

- problem: while we cannot simulate a linear tape, we still have a lot of configurations!

We look into internal symmetries of FRAs:

- **symbolic reasoning**: we are only look at how configurations are related, not their actual content
- **group representations**: we express these interrelations compactly via permutation groups
- **bounded history**: it suffices to consider histories of size up to $2R$

PSPACE solvability

$$\sim^0 \supseteq \sim^1 \supseteq \sim^2 \supseteq \dots \supseteq \sim^i \supseteq \dots \quad \text{and} \quad \sim_s = \bigcup_{i \in \omega} \sim^i$$

Reasoning symbolically:

- each decrease in the indexed chain can be traced back to one of polynomially many factors!

use the fact that strict subgroup chains have bounded length

This means there is a **final polynomial-size i**

- polynomial bound for bisimulation game \rightarrow APTIME

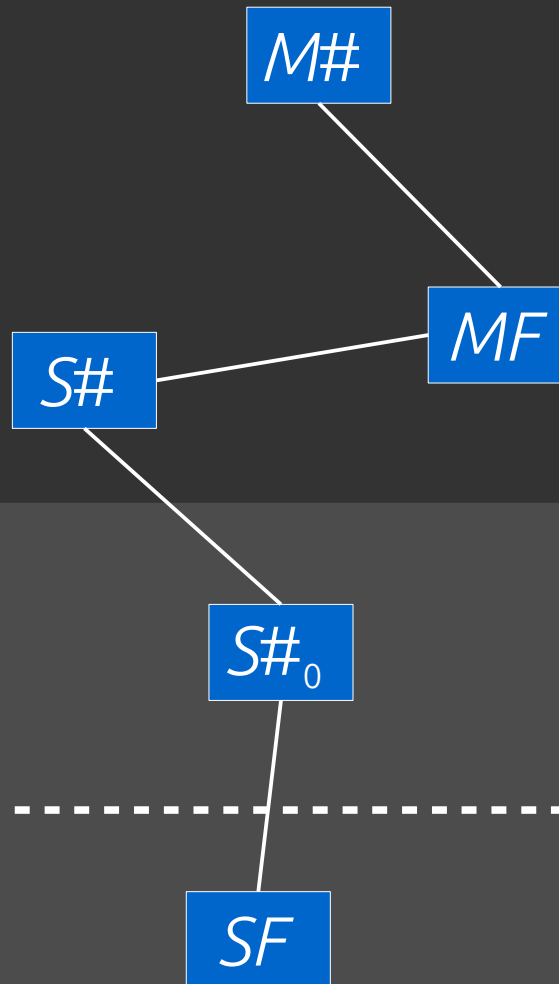
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EXPTIME-c

PSPACE

PSPACE

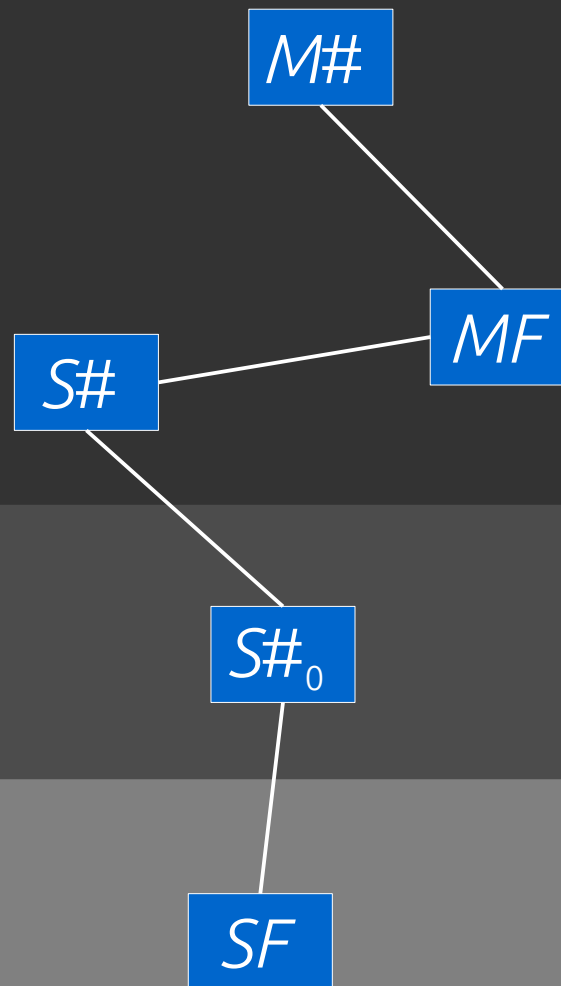
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EXPTIME-c

PSPACE-c

NP

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- History Register Automata (cf. DA/CMA)

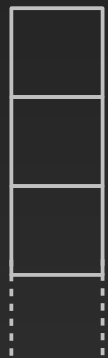
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Pushdown FRAs (FPDRAs)

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*finitely many
(say R) registers*



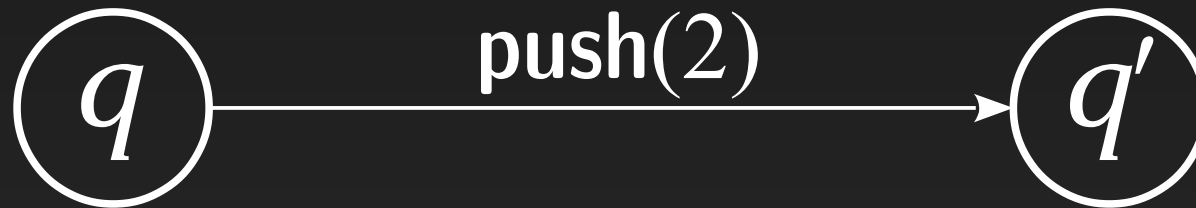
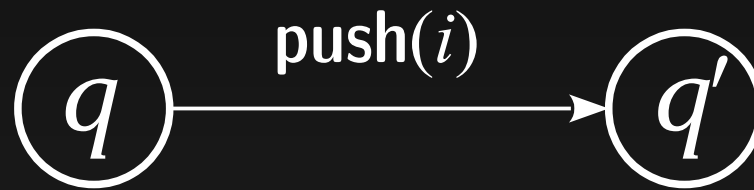
pushdown stack

registers & stack store names

Label λ of the form:

- **reg**(i), $i \in \{1, \dots, R\}$
- ...
- **push**(i), $i \in \{1, \dots, R\}$
- **pop**(i), $i \in \{1, \dots, R\}$
- **pop-diff**

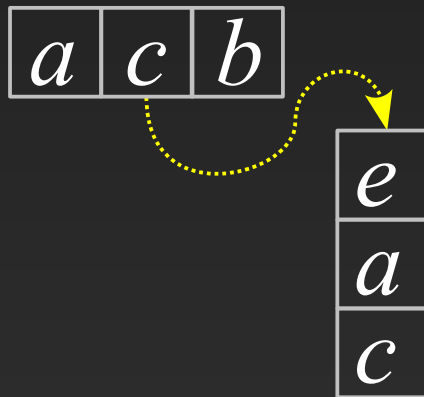
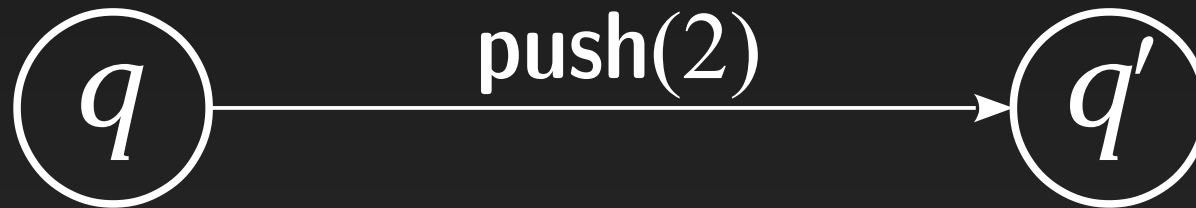
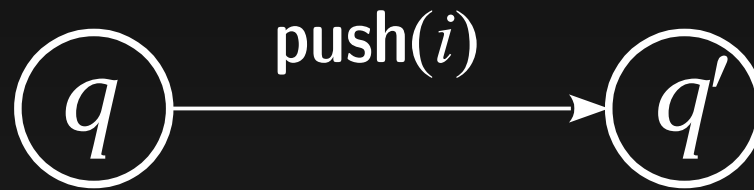
Transitions:



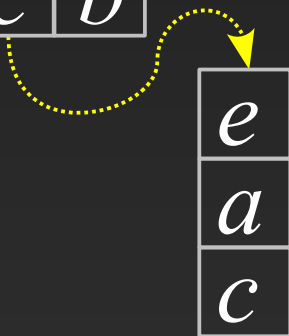
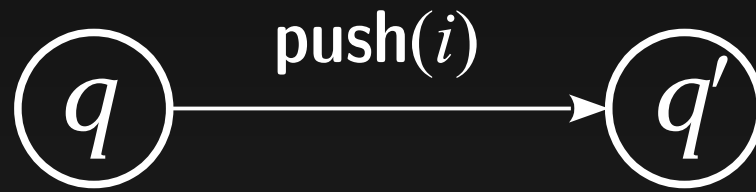
a	c	b
-----	-----	-----

e
a
c

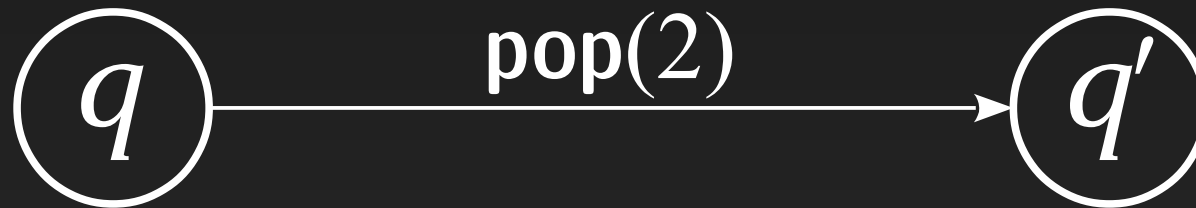
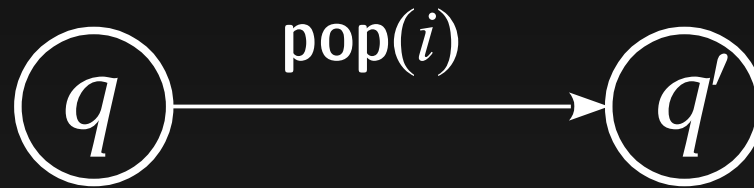
Transitions:



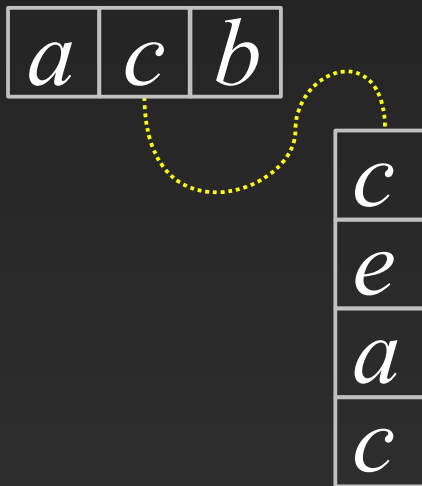
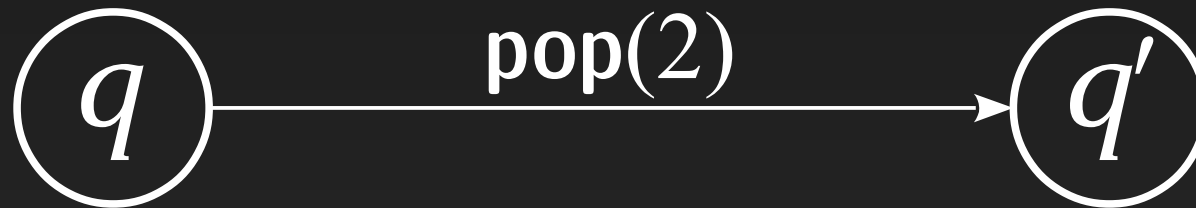
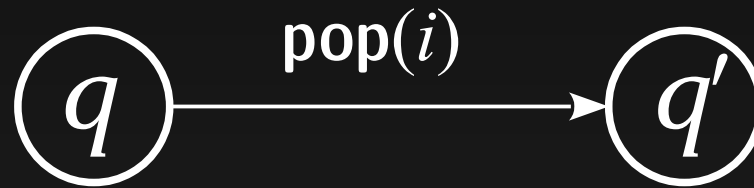
Transitions:



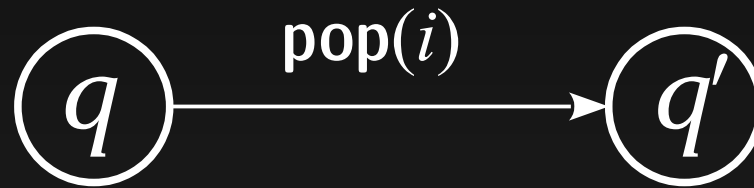
Transitions:



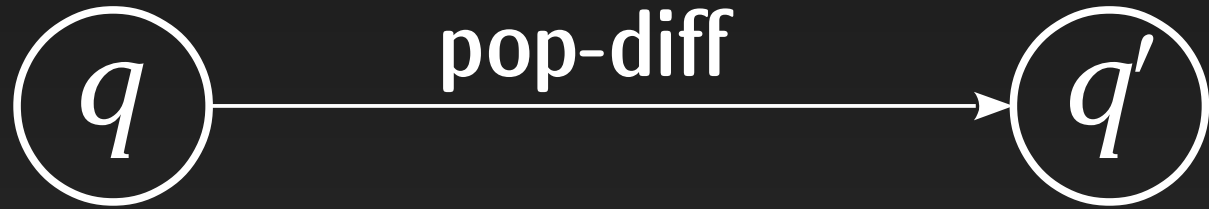
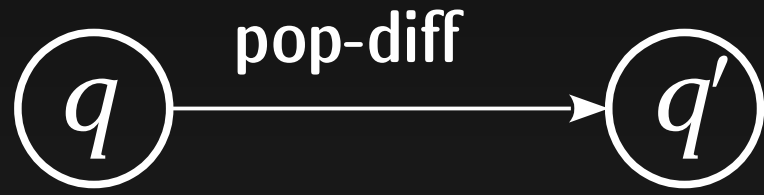
Transitions:



Transitions:



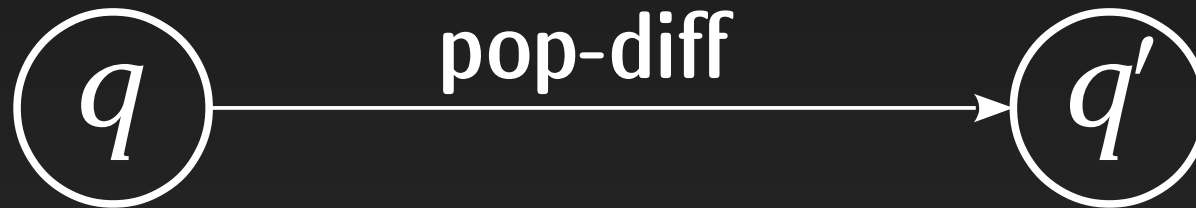
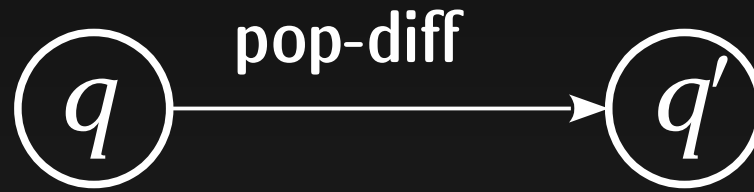
Transitions:



a	c	b
-----	-----	-----

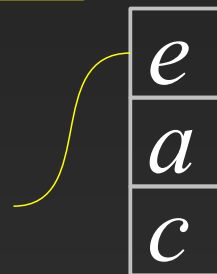
e
a
c

Transitions:

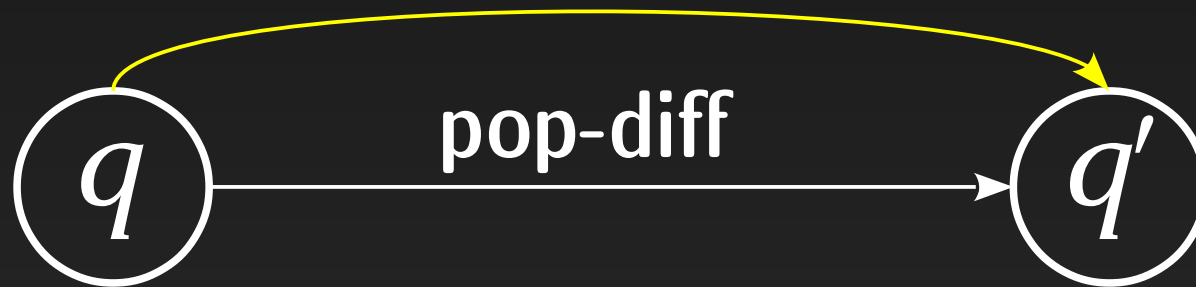
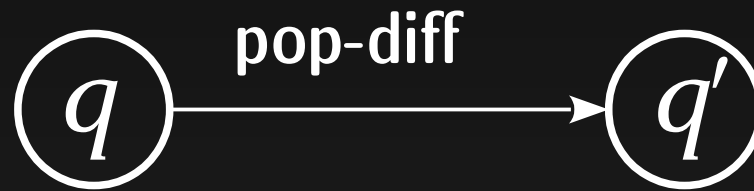


a c b

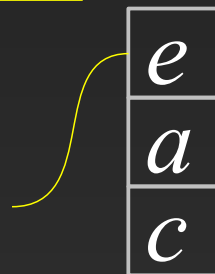
*different
from
current
registers*



Transitions:

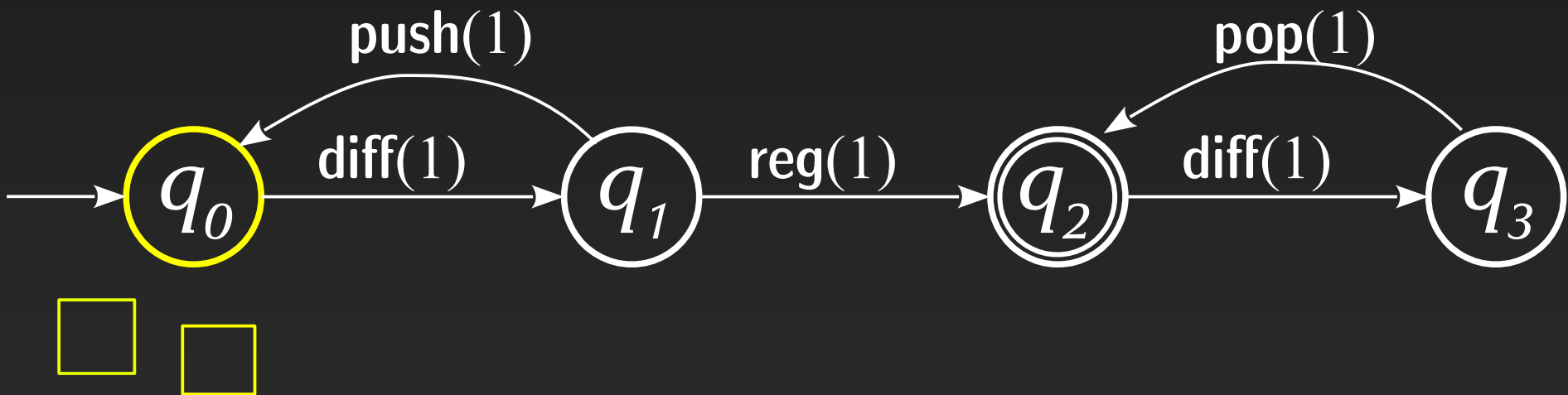


*different
from
current
registers*



Example

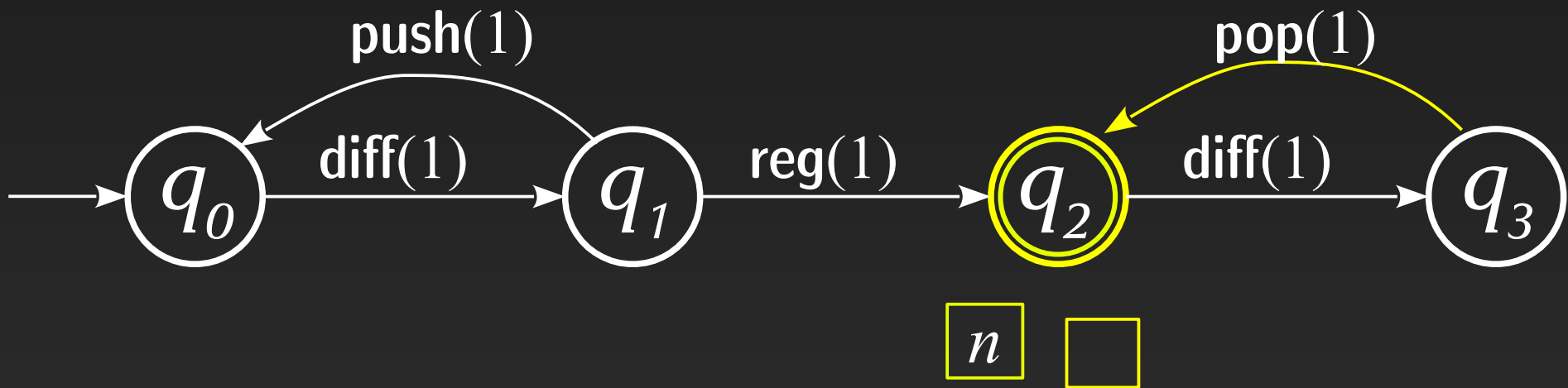
$$L_4 = \{ a_1 a_2 \dots a_n a_n \dots a_2 a_1 \in \Sigma^* \mid n \geq 0, \forall i < n. a_i \neq a_{i+1} \}$$



Example

$$L_4 = \{ a_1 a_2 \dots a_n a_n \dots a_2 a_1 \in \Sigma^* \mid n \geq 0, \forall i < n. a_i \neq a_{i+1} \}$$

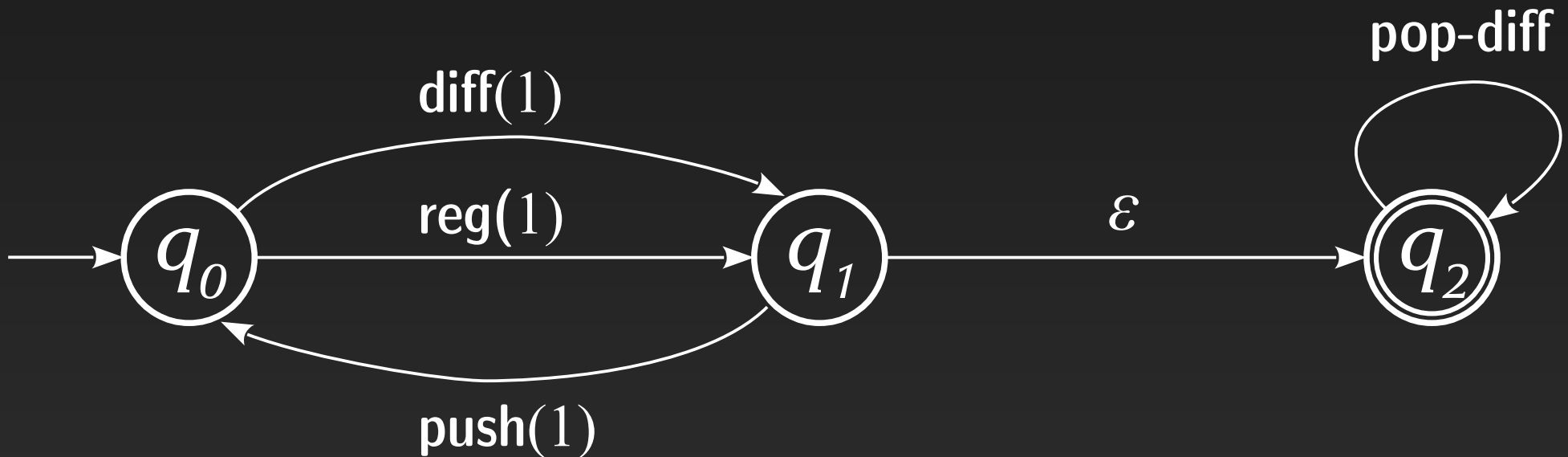
neveroddoeven



Example

$$L_5 = \{ a_1 a_2 \dots a_n b \in \Sigma^* \mid n \geq 0, \forall i \leq n. a_i \neq b \}$$

(all strings where last name is distinct from all previous ones)



Limited distinguish-ability

$$L_{\text{fr}} = \{ a_1 a_2 \dots a_n \in \Sigma^* \mid n \geq 0, \forall i \neq j. a_i \neq a_j \}$$

(all strings of distinct names)

Limited distinguish-ability

$$L_{\text{fr}} = \{ a_1 a_2 \dots a_n \in \Sigma^* \mid n \geq 0, \forall i \neq j. a_i \neq a_j \}$$

(all strings of distinct names)

3R property:

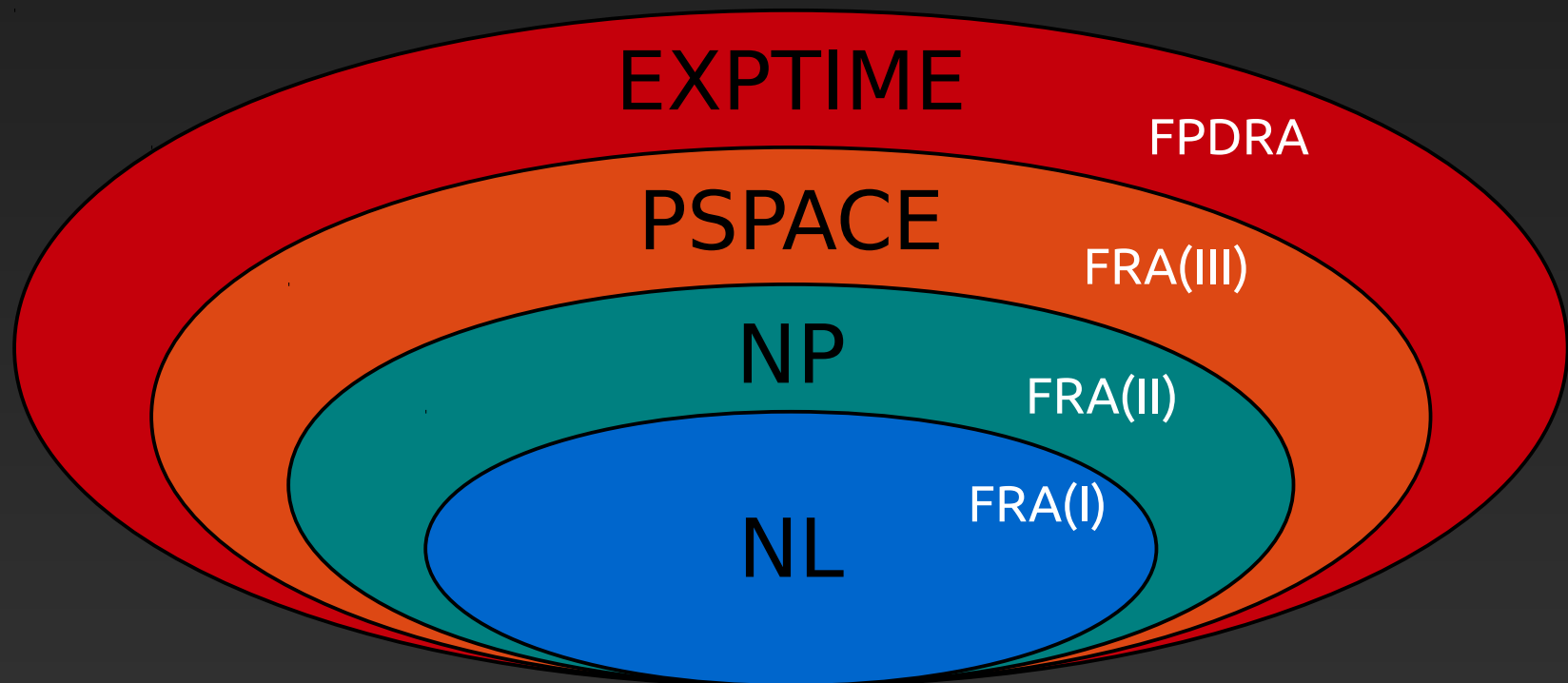
Given a PDRA (no fresh) with R registers with states q_1, q_2 , any run between them (from empty stack to empty stack) can be taken with at most $3R$ names.

Conversely, there is a PDRA with R registers whose runs to a designated state involve exactly $3R$ names.

Reachability/non-emptiness for (F)RAs

→ R -FPRDA Reachability is EXPTIME-complete

- upper bound by $3R + \text{freshness simulation}$ [Murawski & T. 12]
- hardness by reduction from TMs with stack (SF, no fresh)



Investigations in FRAs

Bisimilarity for FRAs (complexity)

- Depends on register mode ($NP \rightarrow PSPACE \rightarrow EXPTIME$)
 - approach uses permutation group theory

[Murawski, Ramsay & T. '15]

Context-freeness: Pushdown FRA

[Cheng & Kaminski '98; Segoufin '06]

[Murawski & T. '12]

- Reachability EXPTIME-complete
- Global reachability via “saturation”

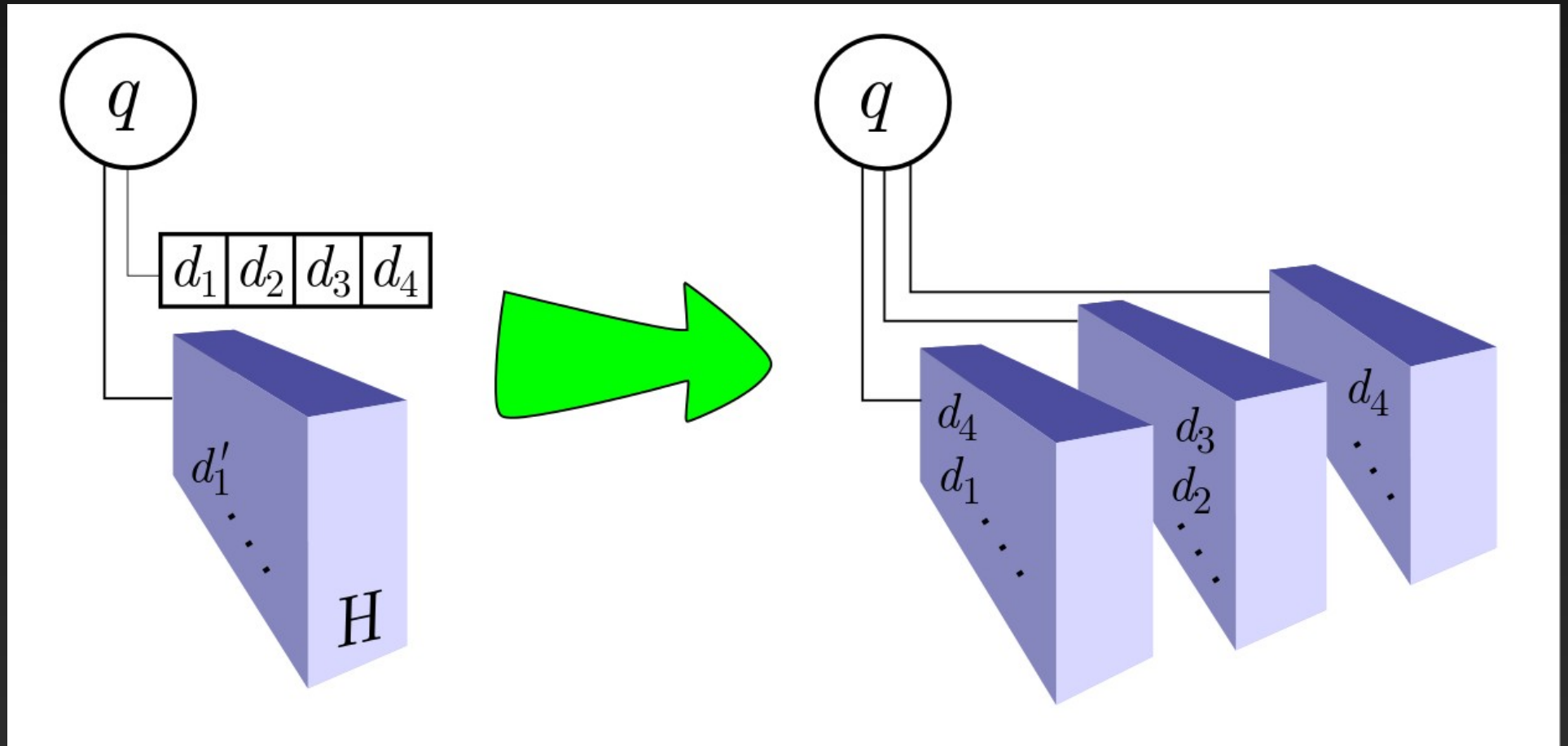
[Murawski, Ramsay & T. '14]

Freshness oracle: from one to many histories

- History Register Automata (cf. DA/CMA)

[Grigore & T. '13]

HRAs: from registers to histories



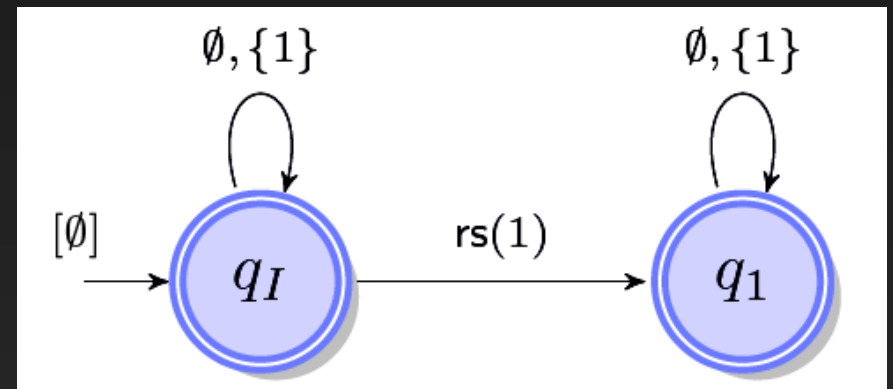
histories = registers with unboundedly many equivalent elements

Expressivity

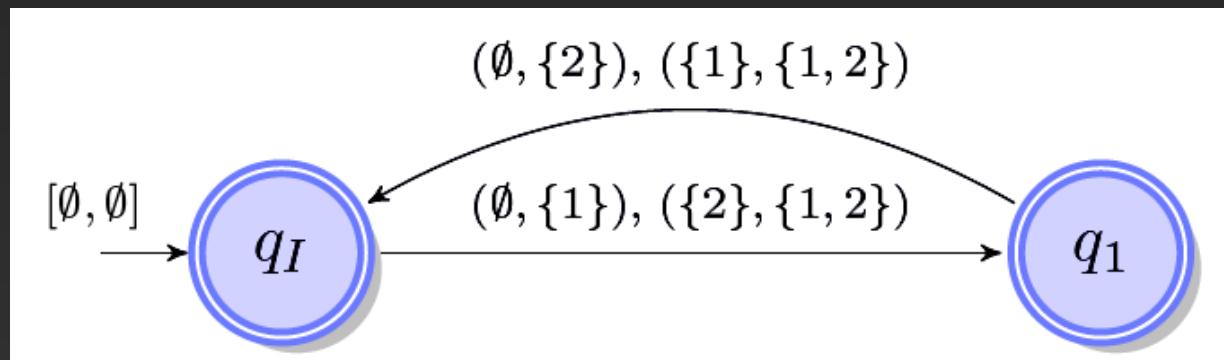
Histories simulate registers \rightarrow HRAs extend FRAs

Histories can be reset:

\rightarrow Closure under Kleene* and concatenation



Several histories \rightarrow Closure wrt interleavings



HRA properties

- Cleanly extend RAs and FRAs
- Closed under all regular operations apart from complementation
- Closed under interleaving
- Universality undecidable (from RAs)
- Emptiness decidable, non-primitive recursive complexity (~transfer/reset Petri nets)
- Closely related to Data / Class Memory Automata

Concluding

Fresh-Register Automata:

- Class of automata over infinite alphabets
“natural” for computation with names/resources
- new landscape of algorithms and results
- applications in verification

Further on:

- open/working problems: automata learning, regular expressions, infinite words, verification logics
- algorithm implementations (an FRA toolkit!)